

10895 U.S. PTO
09/975840
09/17/91

Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>IK3</i> O.A. <i>MIT</i>	

APPLICATION NO. 09/975840	CONT/PRIOR	CLASS 438	SUBCLASS 652	ART UNIT 2812 8805	EXAMINER <i>Kerhava</i>
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APPLICANTS Min-Hsiung Chiang

TITLE Compatible embedded DRAM process for MIM capacitor

PTO-2040
12/93

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					

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<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration of _____ of U.S. Patent No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	(Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
	(Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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